



Fig. 1

<u>Memory Address</u>	<u>Data Point</u>	<u>Data Point Memory Index</u>
FB902834	0	000
FB902836	1	001
FB902838	2	010
FB90283A	3	011
FB90283C	4	100
FB90283E	5	101
FB902840	6	110
FB902842	7	111

Memory Space X

<u>Memory Address</u>	<u>Data Point</u>	<u>Data Point Memory Index</u>
AC1252A0	8	000
AC1252A2	9	001
AC1252A4	10	010
AC1252A6	11	011
AC1252A8	12	100
AC1252AA	13	101
AC1252AC	14	110
AC1252AE	15	111

Memory Space Y

Fig. 2

00000000 00000000

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<u>Data Point</u>	<u>Data Point Memory Index</u>	<u>Parity</u>
0,8	000	0
1,9	001	1
2,10	010	1
3,11	011	0
4,12	100	1
5,13	101	0
6,14	110	0
7,15	111	1

Fig. 3

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Memory Address	Data Point	Data Point Memory Index
AC1252A0	8	000
AC1252A2	1	001
AC1252A4	2	010
AC1252A6	11	011
AC1252A8	4	100
AC1252AA	13	101
AC1252AC	14	110
AC1252AE	7	111

Memory Space Y

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Memory Address	Data Point	Data Point Memory Index
FB902834	0	000
FB902836	9	001
FB902838	10	010
FB90283A	3	011
FB90283C	12	100
FB90283E	5	101
FB902840	6	110
FB902842	15	111

Memory Space X

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Fig. 4